

FIGURE 1

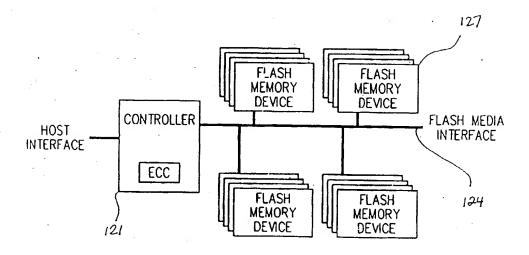
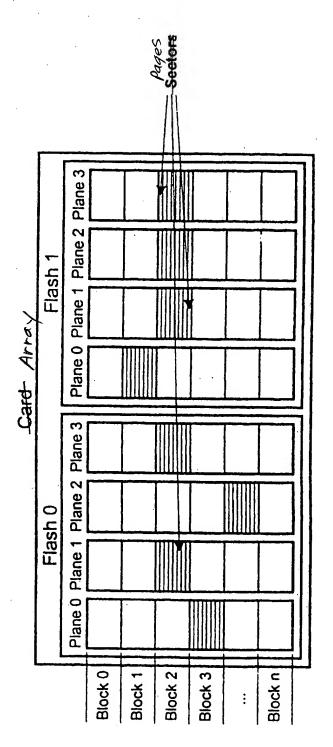


FIGURE 2





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	Plane 0	Plane 1	Plane 2	Plane 3
	00	n+1	2n+1	3n+1
-	01	n+2	2n+2	3n+2
	02	n+3	2n+3	3n+3
		,.		
4	n	2n	3n	4n
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FIGURE 8-5

Plane 0	Plane 1	Plane 2	Plane 3
			-
00	01	02	03
04	05	06	07
08	09	OA	0B
•••	:	•••	
4n - 3	4n-2	40-1	40
			L

FIGURE 94

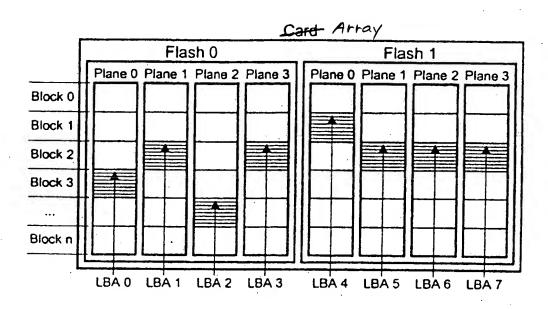


FIGURE 10 6

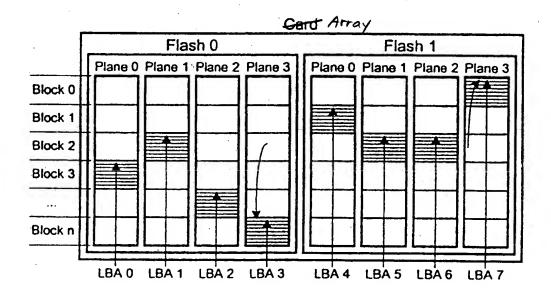


FIGURE # 7

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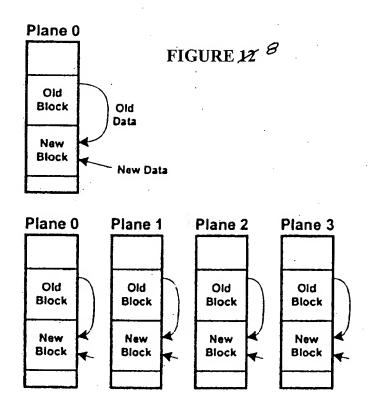


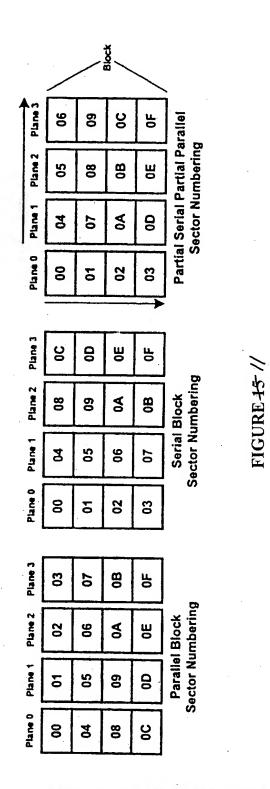
FIGURE 139

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Appln. No.: Unassigned Docket No.: SNDK.334US0
Title: Adaptive Mode Switching of Flash memory Address...
Inventors: Gonzalez et al. Exp. Mail: EV321705370US
Filing Date: Herewith Atty. Tel: (415) 318-1160 Sheet 7 of 9

5NOK.334U50 (7 of 9)

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Sector 31		1					



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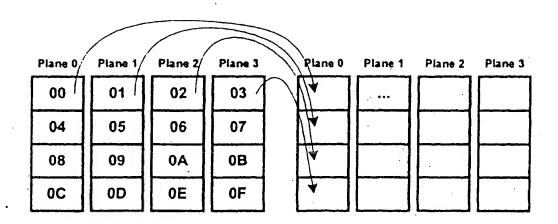


FIGURE 16-12

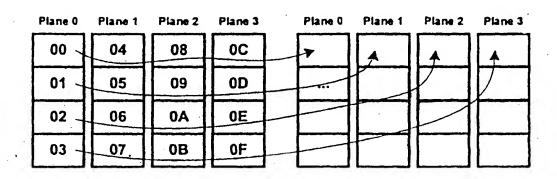


FIGURE 17.13